

```

(0) ((((((vertical adj3 transistor).ti.) not (((ver-
(21) (vertical adj transistor) and (monocrystalline a
(91) (vertical adj transistor) and (plug with (dielec
(70) ((vertical adj transistor) and (plug with (diel
(5) (((vertical adj transistor) and (plug with (diel
(65) (((vertical adj transistor) and (plug with (die
(61) (((vertical adj transistor) and (plug with (di
(49) (((vertical adj transistor) and (plug with (c
(12) (((vertical adj transistor) and (plug with (c
(642) (vertical adj transistor) and (trench with (die
(574) ((vertical adj transistor) and (trench with (di
(566) (((vertical adj transistor) and (trench with (c
(125) (((vertical adj transistor) and (trench with (
(111) (((vertical adj transistor) and (trench with
(103) (((vertical adj transistor) and (trench with
(22) (((vertical adj transistor) and (trench with

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D8a	USPAT, US-PG PUB, EPO, JPO, IBM, TDB
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R. Purcell

Default operator: OR

- Highlight all hit terms initially

```
((vertical adj transistor) and (plug with (dielectric or
insulat$4))) not ((vertical adj transistor) and (monocrystalline adj
silicon) and (plug with (dielectric or insulat$4))) and
(monocrystalline)
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	U	I	PT	P	Document ID	Issue Dat	Pages	Title	Current OR	Current XRef	R	Inventor	S	C
1					US 6207977 B1	20010327	63	Vertical MISFET devices	257/192	257/19; 257/194; 257/331;		Augusto, Carlos Jorge Ramiro Proenca		
2					US 5963800 A	19991005	60	CMOS integration process having vertical channel	438/212	257/334; 257/E21.643; 257/E27.062;		Augusto, Carlos Jorge Ramiro Proenca		
3					US 5920088 A	19990706	61	Vertical MISFET devices	257/192	257/194; 257/330; 257/E21.643;		Augusto, Carlos Jorge Ramiro Proenca		
4					US 5914504 A	19990622	60	DRAM applications using vertical MISFET devices	257/192	257/195; 257/200; 257/201;		Augusto, Carlos Jorge Ramiro Proenca		
5					US 5010386 A	19910423	8	Insulator separated vertical CMOS	257/369	257/328; 257/623; 257/E27.062		Groover, III, Robert		